

10Gb/s SFP+ 1550nm 80km Optical Transceiver Module HTSX-S5596Zx

Features

- Up to 11.3Gb/s data links
- 1550nm EML transmitter and APD receiver
- Up to 80km on 9/125µm SMF
- Hot-pluggable SFP+ footprint
- Duplex LC/UPC type pluggable optical interface
- Metal enclosure, for lower EMI
- RoHS compliant and lead-free
- Support Digital Diagnostic Monitoring interface
- Single +3.3V power supply
- Maximum power consumption 1.5W
- Compliant with SFF+MSA and SFF8472
- Case operating temperature

Commercial: $0 \sim +70^{\circ}$ C Extended: $-10 \sim +80^{\circ}$ C Industrial: $-40 \sim +85^{\circ}$ C



Applications

- 10GBASE-ZR/ZW & 10G Ethernet
- SDH STM64
- Other Optical Links

Part Number Ordering Information

Part Number	Data Rate (Gb/s)	Wavelength (nm)	Transmission Distance(km)	Temperature (°C) (Operating Case)
HTSX-S5596ZC	10.3125	1550	80km SMF	0~70 commercial
HTSX-S5596ZE	10.3125	1550	80km SMF	-10~80 Extended
HTSX-S5596ZI	10.3125	1550	80km SMF	-40~85 Industrial

1. Absolute Maximum Ratings



It has to be noted that the operation in excess of any individual absolute maximum ratings might cause permanent damage to this module.

Parameter	Symbol	Min	Max	Unit	Notes
Storage Temperature	Ts	-40	85	°C	
Power Supply Voltage	V _{CC}	-0.5	3.6	V	
Relative Humidity (non-condensation)	RH	5	95	%	
Damage Threshold	TH₀	0		dBm	

2. Recommended Operating Conditions and Power Supply Requirements

Parameter	Symbo	Min	Typica I	Max	Unit	Notes
Operating Case	T _{OP}	0		70	°C	commerci al
Temperature		-40		85	°C	Industrial
Power Supply Voltage	Vcc	3.135	3.3	3.465	V	
Data Rate			10.312 5		Gb/s	
Control Input Voltage High		2		Vcc	V	
Control Input Voltage Low		0		0.8	V	
Link Distance (SMF)	D			80	km	9/125um

3. General Description

HTF's HTSX-S5596Zx SFP+ transceiver is designed for use in 10-Gigabit Ethernet links up to 80km over single mode fiber. The module consists of 1550 EML Laser, APD and Preamplifier in a high-integrated optical sub-assembly. Digital diagnostics functions are available via a 2-wire serial interface, as specified in SFF-8472.

HTSX-S5596Zx transceivers provide a unique enhanced digital diagnostic monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage. It also defines a sophisticated system of alarm and warning flags, which alerts end-users when particular operating parameters are outside of a factory set normal range.



The SFP+ MSA defines a 256-byte memory map in EEPROM that is accessible over a 2-wire serial interface at the 8 bit address 1010000X (A0h). The digital diagnostic monitoring interface makes use of the 8 bit address 1010001X (A2h), so the originally defined serial ID memory map remains unchanged.

4. Pin Assignment and Pin Description

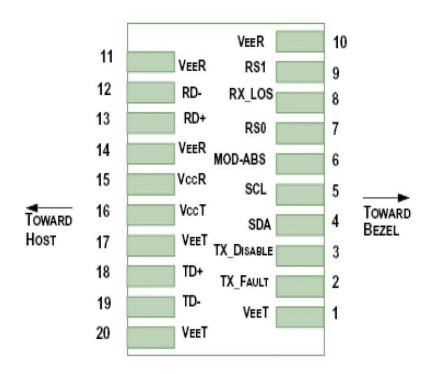


Figure 1. Diagram of host board connector block pin numbers and names

Pin	Symbol	Name/Description	Notes
1	V_{EET}	Transmitter Ground (Common with Receiver Ground)	1
2	T _{FAULT}	Transmitter Fault.	2
3	T _{DIS}	Transmitter Disable. Laser output disabled on high or open.	3
4	SDA	2-wire Serial Interface Data Line	4
5	SCL	2-wire Serial Interface Clock Line	4
6	MOD_ABS	Module Absent. Grounded within the module	4
7	RS0	Rate Select 0	5
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.	6
9	RS1	No connection required	



10	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1
11	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1
12	RD-	Receiver Inverted DATA out. AC Coupled	
13	RD+	Receiver Non-inverted DATA out. AC Coupled	
14	$V_{_{EER}}$	Receiver Ground (Common with Transmitter Ground)	1
15	V _{CCR}	Receiver Power Supply	
16	V _{CCT}	Transmitter Power Supply	
17	V _{EET}	Transmitter Ground (Common with Receiver Ground)	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	V_{EET}	Transmitter Ground (Common with Receiver Ground)	1

Notes:

- 1. Circuit ground is internally isolated from chassis ground.
- 2. TFAULT is an open collector/drain output, which should be pulled up with a $4.7k\Omega-10~k\Omega$ resistor on the host board if intended for use. Pull up voltage should be between 2.0V to Vcc + 0.3V.A high output indicates a transmitter fault caused by either the TX bias current or the TX output power exceeding the preset alarm thresholds. A low output indicates normal operation. In the low state, the output is pulled to <0.8V.
- 3. Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
- 4. Should be pulled up with $4.7k\Omega$ $10k\Omega$ on host board to a voltage between 2.0V and 3.6V. MOD_ABS pulls line low to indicate module is plugged in.
- 5. Internally pulled down per SFF-8431 Rev 4.1.
- 6. LOS is open collector output. It should be pulled up with $4.7k\Omega 10k\Omega$ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.

5. Electrical Characteristics

The following electrical characteristics are defined over the Recommended Operating Environment unless otherwise specified.

Parameter	Symbol	Min.	Тур.	Max	Unit	Notes	
Power Consumption	р			1.5	W		
Supply Current	lcc			450	mA		
Transmitter							
Single-ended Input Voltage Tolerance	Vcc	-0.3		4.0	V		



AC Common Mode Input Voltage Tolerance (RMS)		15			mV	
Differential Input Voltage Swing	Vin,pp	180		700	mVpp	
Differential Input Impedance	Zin	90	100	110	Ohm	1
Transmit Disable Assert Time				10	us	
Transmit Disable Voltage	Vdis	Vcc-1.3		Vcc	V	
Transmit Enable Voltage	Ven	Vee		Vee +0.8	V	2
	Red	ceiver				
Differential Output Voltage Swing	Vout,pp	300		850	mVpp	
Differential Output Impedance	Zout	90	100	110	Ohm	3
Data output rise/fall time	Tr/Tf	28			ps	4
LOS Assert Voltage	VlosH	Vcc-1.3		Vcc	V	5
LOS De-assert Voltage	VlosL	Vee		Vee +0.8	V	5
Power Supply Rejection	PSR	100			mVpp	6

Notes:

- 1. Connected directly to TX data input pins. AC coupled thereafter.
- 2. Or open circuit.
- 3. Input 100 ohms differential termination.
- 4. These are unfiltered 20-80% values.
- 5. Loss of Signal is LVTTL. Logic 0 indicates normal operation; logic 1 indicates no signal detected.
- 6. Receiver sensitivity is compliant with power supply sinusoidal modulation of 20 Hz to 1.5 MHz up to specified value applied through the recommended power supply filtering network.

6. Optical Characteristics

The following optical characteristics are defined over the Recommended Operating Environment unless otherwise specified

Parameter	Symbol	Min.	Typical	Max	Unit	Notes
Transmitter						
Center Wavelength	λς	1530	1550	1565	nm	1
Optical Spectral Width	Δλ			1	nm	
Side Mode Suppression Ratio	SMSR	30			dB	
Average Optical Power	P _{AVG}	-1		4	dBm	2



Optical Extinction Ratio	ER	6			dB	
Transmitter and Dispersion Penalty	TDP			3.0	dB	
Transmitter OFF Output Power	Poff			-30	dBm	
Transmitter Eye Mask		Compliant	with IEEE8	302.3ae		
	R	eceiver				
Center Wavelength	λc	1270		1610	nm	
Receiver Sensitivity (Average Power)	Sen.			-23	dBm	3
Input Saturation Power (overload)	Psat	-6			dBm	
Receiver Reflectance	Rrx			-27		
LOS Assert	LOSA	-32			dBm	
LOS De-assert	LOSD			-26	dBm	
LOS Hysteresis	LOSH	0.5			dB	

Notes:

- 1. Class 1 Laser Safety per FDA/CDRH and IEC-825-1 regulations.
- 2. Launched power (avg.) is power coupled into a single mode fiber with master connector (Before of Life) .
- 3. Measured with Light source 1550nm, ER=6.0dB; BER =<10^-12 @10.3125Gbps, PRBS=2^31-1 NRZ.

7. Digital Diagnostic Functions

The following digital diagnostic characteristics are defined over the Recommended Operating Environment unless otherwise specified. It is compliant to SFF8472 Rev10.2 with internal calibration mode. For external calibration mode please contact our sales staff.

Parameter	Symbol	Min.	Max	Unit	Notes
Temperature monitor absolute error	DMI_ Temp	-3	3	degC	Over operating temp
Supply voltage monitor absolute error	DMI_VCC	- 0.15	0.15	V	Full operating range
RX power monitor absolute error	DMI_RX	-3	3	dB	
Bias current monitor	DMI_ bias	- 10%	10%	mA	
TX power monitor absolute error	DMI_TX	-3	3	dB	



8. Mechanical Dimensions

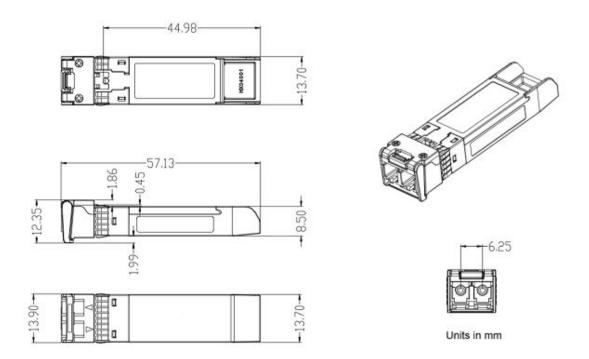


Figure 2. Mechanical Outline